	Application No.	Applicant(s)	
	10/647,608	KELLER ET AL.	(M)
Notice of Allowability	Examiner	Art Unit	
	Vuthe Siek	2825	
The MAILING DATE of this communication appeall claims being allowable, PROSECUTION ON THE MERITS IS nerewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RID of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this apport or other appropriate communication GHTS. This application is subject to	olication. If not included will be mailed in due co	d ourse. THIS
1. X This communication is responsive to 5/23/05 and interview	<u>on 5/28/05</u> .		
2. X The allowed claim(s) is/are <u>1-15</u> .			
3. $igotimes$ The drawings filed on <u>25 August 2003</u> are accepted by the	Examiner.		
 Acknowledgment is made of a claim for foreign priority unally all b) Some* c) None of the: Certified copies of the priority documents have Certified copies of the priority documents have Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). 	been received. been received in Application No	· national stage application	on from the
Applicant has THREE MONTHS FROM THE "MAILING DATE" of noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	• •	complying with the requ	iirements
5. A SUBSTITUTE OATH OR DECLARATION must be submit INFORMAL PATENT APPLICATION (PTO-152) which give			TICE OF
6. CORRECTED DRAWINGS (as "replacement sheets") mus (a) including changes required by the Notice of Draftspers 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Paper No./Mail Date Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in the	on's Patent Drawing Review (PTO-9 Amendment / Comment or in the O 84(c)) should be written on the drawing	ffice action of	back) of
7. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT I			te the
Attachment(s)			
1. Notice of References Cited (PTO-892)	5. ☐ Notice of Informal Pa		·152)
 Notice of Draftperson's Patent Drawing Review (PTO-948) Information Disclosure Statements (PTO-1449 or PTO/SB/0 	6. ⊠ Interview Summary Paper No./Mail Date 8), 7. ⊠ Examiner's Amendr	ė	·
Paper No./Mail Date <u>5/23/05</u> 1. Examiner's Comment Regarding Requirement for Deposit	8. Examiner's Stateme		vance
of Biological Material	9. Other	John Stor	anoc
		VUTHE SIEK	
	PF	RIMARY EXAMINER	•

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DETAILED ACTION

1. This office action is in response to application 10/647,608 and response filed on 5/23/2005. Claims 1-15 remain pending in the application.

Examiner's Amendment

2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Curtis A. Vock (38,356) on 7/28/05.

The application has been amended as follows:

In specification on page 1, delete whole paragraph [0001], and insert:

The present document contains material related to the material of copending, cofiled, U.S. Patent Application Number 10/647,597, entitled System And Method For Determining Wire Capacitance For A VLSI Circuit; U.S. Patent Application Number 10/647,595, entitled System And Method For Determining Applicable Configuration Information For Use In Analysis Of A Computer Aided Design; U.S. Patent Application Number 10/647,687, entitled Systems And Methods Utilizing Fast Analysis Information During Detailed Analysis Of A Circuit Design; U.S. Patent Application Number 10/647,594, entitled Systems And Methods For Determining Activity Factors Of A Circuit Design; U.S. Patent Application Number 10/647,768, entitled System And Method For Determining A Highest Level Signal Name In A Hierarchical VLSI Design; U.S. Patent Application Number 10/647,606, entitled System And Method For Determining Connectivity Of Nets In A Hierarchical Circuit Design; U.S. Patent Application Number 10/647,596, entitled System And Method Analyzing Design Elements In Computer Aided Design Tools; U.S. Patent Application Number 10/647,598, entitled Computer Aided Design Systems And

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Methods With Reduced Memory Utilization; U.S. Patent Application Number 10/647,688, entitled System And Method For Iteratively Traversing A Hierarchical Circuit Design; U.S. Patent Application Number 10/647,769, entitled Systems And Methods For Establishing Data Model Consistency Of Computer Aided Design Tools; U.S. Patent Application Number 10/647,607, entitled Systems And Methods For Identifying Data Sources Associated With A Circuit Design; and U.S. Patent Application Number 10/647,605, entitled Systems And Methods For Performing Circuit Analysis On A Circuit Design, the disclosures of which are hereby incorporated herein by reference.--

The above changes update the related applications information.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Vuthe Siek whose telephone number is (703) 305-4958. The examiner can normally be reached on M-F (6:30-4:00) 2nd Friday off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew Smith can be reached on (703) 308-1323. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

VUTHE SIEK PRIMARY EXAMINER